

ECE 327: Digital Circuit Design

Instructor: Dr. Ning Weng

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EGRE 119

Class website: <http://www.engr.siu.edu/~weng/ece327>

Lecture: MWF 1:00 pm to 1:50 pm EGRA 220

Lab Sec. 1: R 8:00 am to 10:50 am EGRE 237

Lab Sec. 2: T 2:00 am to 04:50 pm EGRE 237

Office Hours: MW 2:15 to 3:45 pm or by appointment

TA: Lucas John Vespa

lukevespa@gmail.com

Textbook: “Digital Design from Zero to One” by Jerry D. Daniels, John Wiley and Sons, Inc., New York, 1996.

References:

1. “Digital Logic Circuit Analysis and Design” by Victor P. Nelson, H. Troy Nagle, Bill D. Carroll and David Irwin, Prentice Hall, Englewood Cliffs, New Jersey, 1995.
2. “Digital Logic Design Principles” by Norman Balabanian, Bradley Carlson, John Wiley and Sons, Inc., New York, 2000.

Course Topics:

- Review of combinational analysis and design
- Introduction to sequential devices
- Moore model analysis and design
- Mealy model analysis and design
- Modular sequential design

Grading:

Exam I	20%
Exam II	20%
Laboratory	20%
Homework	15%
Quizzes	5%
Final Exam	20%

Grades:

≥ 90%	A
≥ 80% <90%	B
≥ 70% <80%	C
≥ 60% <70%	D
< 60%	F

Note: Students are responsible for all announcements made in class or on the Website. Quizzes are announced one class period in advance and posted on the website. No quiz make-up under any circumstances. Late homework is docked 10% per school day up to 50% maximum.