ECE 327: Digital Circuit Design

Instructor: Dr. Ning Weng
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Class website: http://www.engr.siu.edu/~weng/ece327

Lecture: MWF 1:00 pm to 1:50 pm    EGRA 220
Lab Sec. 1: R 8:00 am to 10:50 am    EGRE 237
Lab Sec. 2: T 2:00 am to 04:50 pm    EGRE 237
Office Hours: MW 2:15 to 3:45 pm or by appointment

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References:

Course Topics:
• Review of combinational analysis and design
• Introduction to sequential devices
• Moore model analysis and design
• Mealy model analysis and design
• Modular sequential design

Grading:

Exam I 20%
Exam II 20%
Laboratory 20%
Homework 15%
Quizzes 5%
Final Exam 20%

Grades:

≥ 90%        A
≥ 80% <90%    B
≥ 70% <80%    C
≥ 60% <70%    D
< 60%         F

Note: Students are responsible for all announcements made in class or on the Website. Quizzes are announced one class period in advance and posted on the website. No quiz make-up under any circumstances. Late homework is docked 10% per school day up to 50% maximum.