## National Instruments Breakout Board Pinout CB-68LRP

|   |             | CONNECTOR                        |   |             |
|---|-------------|----------------------------------|---|-------------|
|   |             | CB-68LPR                         |   |             |
| J1<br>J35<br>J2<br>J36<br>J37<br>J4<br>J38<br>J5<br>J39<br>J6<br>J40<br>J7<br>J41<br>J8<br>J42<br>J9<br>J43<br>J41<br>J44 |             | Serial Number  J13               | J68 J34 J67 J33 J66 J32 J65 J31 J64 J30 J63 J29 J62 J28 J61 J27 J60 J26 J26 J25 |             |
| J11<br>J45<br>J12<br>J46  | 0<br>0<br>0 | J22 O J56 O NATIONAL INSTRUMENTS | J58<br>J24<br>J57<br>J23  | 0<br>0<br>0 |



## 68-Pin MIO I/O Connector Pinout

This figure shows the pinout of 68-pin B Series and E Series devices. If you are using the R6850 or SH6850 cable assemblies with 68-pin E Series devices, refer to the 50-Pin MIO pinout.

| AI 8                | 34 | 68 | ALO               |  |  |  |
|---------------------|----|----|-------------------|--|--|--|
| Al 1                | 33 | 67 | AI GND            |  |  |  |
| AI GND              | 32 | 66 | Al 9              |  |  |  |
| Al 10               | 31 | 65 | Al 2              |  |  |  |
| Al 3                | 30 | 64 | AI GND            |  |  |  |
| AI GND              | 29 | 63 | Al 11             |  |  |  |
| Al 4                | 28 | 62 | ALSENSE           |  |  |  |
| AI GND              | 27 | 61 | Al 12             |  |  |  |
| Al 13               | 26 | 60 | Al 5              |  |  |  |
| Al 6                | 25 | 59 | AI GND            |  |  |  |
| AI GND              | 24 | 58 | Al 14             |  |  |  |
| Al 15               | 23 | 57 | Al 7              |  |  |  |
| AO 01               | 22 | 56 | AI GND            |  |  |  |
| AO 11               | 21 | 55 | AO GND            |  |  |  |
| AO EXT REF1         | 20 | 54 | AO GND            |  |  |  |
| P0.4                | 19 | 53 | D GND             |  |  |  |
| D GND               | 18 | 52 | P0.0              |  |  |  |
| P0.1                | 17 | 51 | P0.5              |  |  |  |
| P0.6                | 16 | 50 | D GND             |  |  |  |
| D GND               | 15 | 49 | P0.2              |  |  |  |
| +5 V                | 14 | 48 | P0.7              |  |  |  |
| D GND               | 13 | 47 | P0.3              |  |  |  |
| D GND               | 12 | 46 | AI HOLD COMP      |  |  |  |
| PFI 0/AI START TRIG | 11 | 45 | EXT STROBE        |  |  |  |
| PFI 1/AI REF TRIG   | 10 | 44 | D GND             |  |  |  |
| D GND               | 9  | 43 | PFI 2/AI CONV CLK |  |  |  |
| +5 V                | 8  | 42 | PFI 3/CTR 1 SRC   |  |  |  |
| D GND               | 7  | 41 | PFI 4/CTR 1 GATE  |  |  |  |
| PFI 5/AO SAMP CLK   | 6  | 40 | CTR 1 OUT         |  |  |  |
| PFI 6/AO START TRIG | 5  | 39 | D GND             |  |  |  |
| D GND               | 4  | 38 | PFI 7/AI SAMP CLK |  |  |  |
| PFI 9/CTR 0 GATE    | 3  | 37 | PFI 8/CTR 0 SRC   |  |  |  |
| CTR 0 OUT           | 2  | 36 | D GND             |  |  |  |
| FREQ OUT            | 1  | 35 | D GND             |  |  |  |
|                     |    | /  | /                 |  |  |  |

**Note** Further documentation for the B Series devices is located in the *E Series Help*.

**Note** Some hardware accessories may not yet reflect the revised terminal names. If you are using an E Series device in Traditional NI-DAQ (Legacy), refer to the <u>Terminal Name</u> <u>Equivalents</u> table for information on Traditional NI-DAQ (Legacy) signal names.

<sup>&</sup>lt;sup>1</sup> No connects appear on pins 20 through 22 of devices that do not support AO or use an external reference.

## Old Style Labeling of DAQ board pinouts PCI 6024E

| ACHE                 | 34 | 68  | ACHo               |
|----------------------|----|-----|--------------------|
| ACH1                 | 33 | 67  | AIGND              |
| AIGND                | 32 | 66  | ACH9               |
| ACH10                | 31 | 65  | ACH2               |
| АСНЗ                 | 30 | 64  | AIGND              |
| AIGND                | 29 | 63  | ACH11              |
| ACH4                 | 28 | 62  | AISENSE            |
| AIGND                | 27 | 61  | ACH12              |
| ACH13                | 26 | 60  | ACH5               |
| АСНь                 | 25 | 59  | AIGND              |
| AIGND                | 24 | 58  | ACH14              |
| ACH15                | 23 | 57  | ACH7               |
| DACoOUT <sup>1</sup> | 22 | 56  | AIGND              |
| DAC10UT1             | 21 | 55  | AOGND <sup>1</sup> |
| EXTREF <sup>1</sup>  | 20 | 54  | AOGND <sup>1</sup> |
| D104                 | 19 | 53  | DGND               |
| DGND                 | 18 | 52  | D100               |
| DI01                 | 17 | 51  | DI05               |
| D106                 | 16 | 50  | DGND               |
| DGND                 | 15 | 49  | DI02               |
| +5 V                 | 14 | 48  | D107               |
| DGND                 | 13 | 47  | D103               |
| DGND                 | 12 | 46  | SCANCLK            |
| PFIo/TRIG1           | 11 | 45  | EXTSTROBE*         |
| PFI1/TRIG2           | 10 | 44  | DGND               |
| DGND                 | 9  | 43  | PFI2/CONVERT*      |
| +5 V                 | 8  | 42  | PFI3/GPCTR1_SOURCE |
| DGND                 | 7  | 41  | PFI4/GPCTR1_GATE   |
| PFIS/UPDATE*         | 6  | 40  | GPCTR1_OUT         |
| PR6/WFTRIG           | 5  | 39  | DGND               |
| DGND                 | 4  | 311 | PFIT/STARTSCAN     |
| PFIs/GPCTRo_GATE     | 3  | 37  | PFIN/GPCTRD_SOURCE |
| GPCTRo_OUT           | 2  | 36  | DGND               |
| FREQ_OUT             | 1  | 35  | DGND               |
|                      |    |     |                    |

<sup>&</sup>lt;sup>1</sup> Not available on AT-AI-16XE-10, PCI-6032E, DADCard-A1-16E-4, DADCard-A1-16XE-50

Figure 3. 68-Pin I/O Connector for NI 6070E, NI 6060E, NI 6062E, NI 6052E, NI 6041E, NI 6040E, NI 6036E, NI 6035E, NI 6034E, NI 6032E, NI 6030E, NI 6024E, NI 6023E, NI 6020E, NI 6012E and NI 6011E Devices